



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Naoto HORIGUCHI et al.**

Group Art Unit: **2811**

Serial No.: **09/726,386**

Examiner: **Thien F. TRAN**

Filed: **December 1, 2000**

Confirmation No.: **1274**

For: **SEMICONDUCTOR MEMORY WITH FLOATING GATE TYPE FET**

Attorney Docket No.: **001497**

Customer Number: **38834**

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: March 15, 2004

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated November 13, 2003 for one month from February 13, 2004 to March 13, 2004.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. If any additional fees are due in connection with this paper, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

Thomas E. Brown
Attorney for Applicants
Reg. No. 44,450

TEB/jnj
1250 Connecticut Avenue, NW - Suite 700
Washington, D.C. 20036
(202) 822-1100 (t)
(202) 822-1111 (f)

03/17/2004 WBD/ELR1 00000086 09726386 110.00 OP
01 FC:1251